



H.264 Decoder

Product Data Sheet

v1.1

Updated: April 1, 2010

Features

- Supports MPEG-4, Part 10 video decoding
- Supports H.264 baseline profile (BP) decoding
- Supports ASO and FMO
- Decodes baseline profile streams with level up to 5.1 encoded bit streams at NAL-unit boundary
- Error resilience and concealment tools present
- Configurable to enable hardware de-blocking accelerators
- Configurable output formats, supported output format: IYUV and UYVY fourcc formats
- User configurability for video cropping
- Re-entrant and Re-locatable decoder (support for multi-threaded environment)
- Low latency
- ITU conformance
- GStreamer plugin wrapper for Linux® platforms
- DirectShow filter wrapper for Windows® CE platforms

Supported Platforms

- Hardware – i.MX ARM9™ and ARM11™ platforms
- Software – eLinux, Windows® Embedded CE operating systems

Performance Metrics

i.MX ARM11™ eLinux Platforms

Typical Specifications: CIF, 30fps, 768kbps

Performance (MHz): 236.00

Memory Footprint (KB):

- ROM: 80
- RAM: 1,500

Max System Support: VGA, 15fps, 600kbps

i.MX ARM11™ Windows® CE Platforms

Typical Specifications: CIF, 30fps, 768kbps

Performance (MHz): 312.00

Memory Footprint (KB)

- ROM: 80
- RAM: 1,500

Max System Support: VGA, 15fps, 600kbps

Performance measurements can deviate based on ARM core, memory and cache configuration on the board. To measure directly, enable the TIME_PROFILE in the test application provided in the release package.

i.MX platforms containing VPU hardware acceleration are described in the VPU data sheet.

For further details, contact Freescale customer representative.